

FIG. 1

BEST AVAILABLE COPY

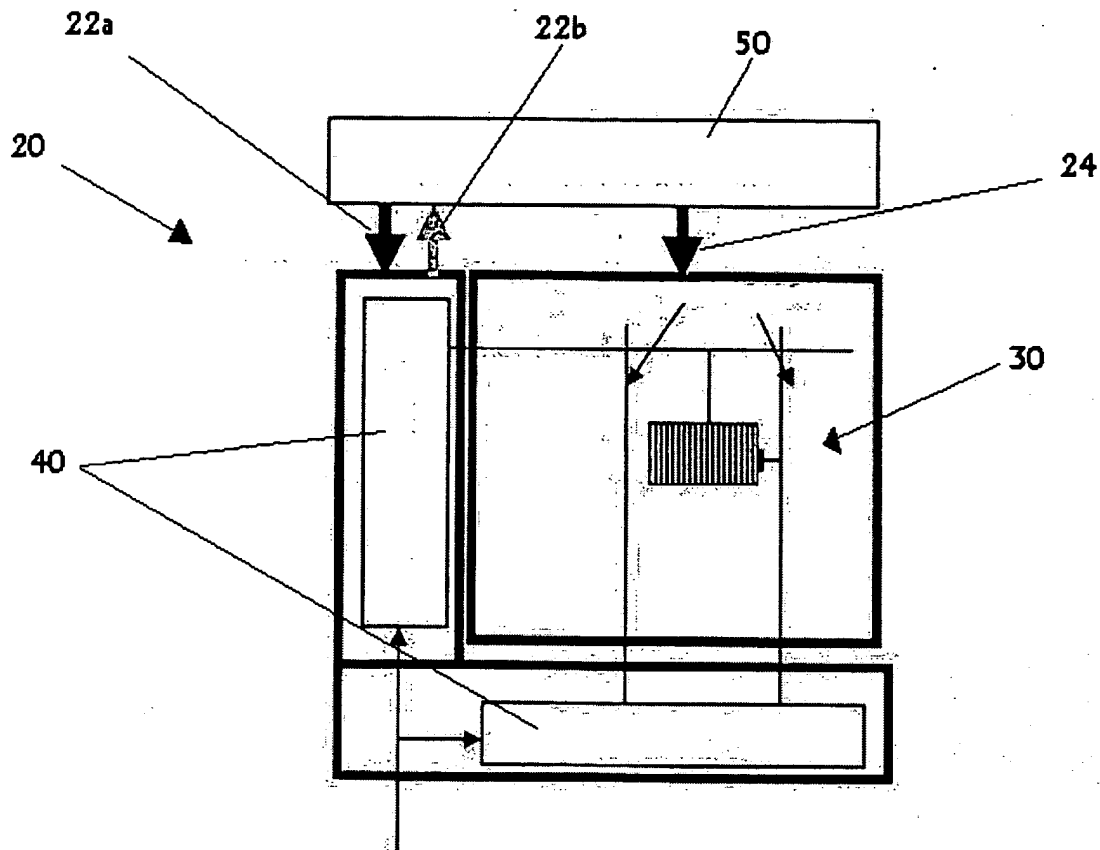


FIG. 2

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Memory Name: [HydraBlox7418F]

Number of Words: [8192]
Number of Bits: [32]
Column-Mux Option: [16]
Frequency (MHz): [10]
Layer Standard File: [/usr/include/uchin/ Brown]
Metal Layers in Product: [5]
Support Top Via: ☐ Yes ☒ No
Ultra low power: ☒ Yes ☐ No

Ring Text Options:
Lower Left: ☒ Yes ☐ No
Lower Right: ☒ Yes ☐ No
Upper Left: ☒ Yes ☐ No
Upper Right: ☒ Yes ☐ No
Nearest File Options:
Vias: ☒ Yes ☐ No
Splice Brackets: ☒ Yes ☐ No

Ring Placement Type:
Type1 describe...
Type2 describe...
Type3 describe...

RING RATIO:
Left: [1.0] Right: [1.0] Top: [1.0]
Width: [20.0] Auto

Pin Routing Layer: [MET1]
Metal Layer: Signal
Internal: [MET1] VSS
External: [MET1] VDD

OK Cancel

Valid Range: 1 to 700

62 →
64 →
60 →

FIG. 3

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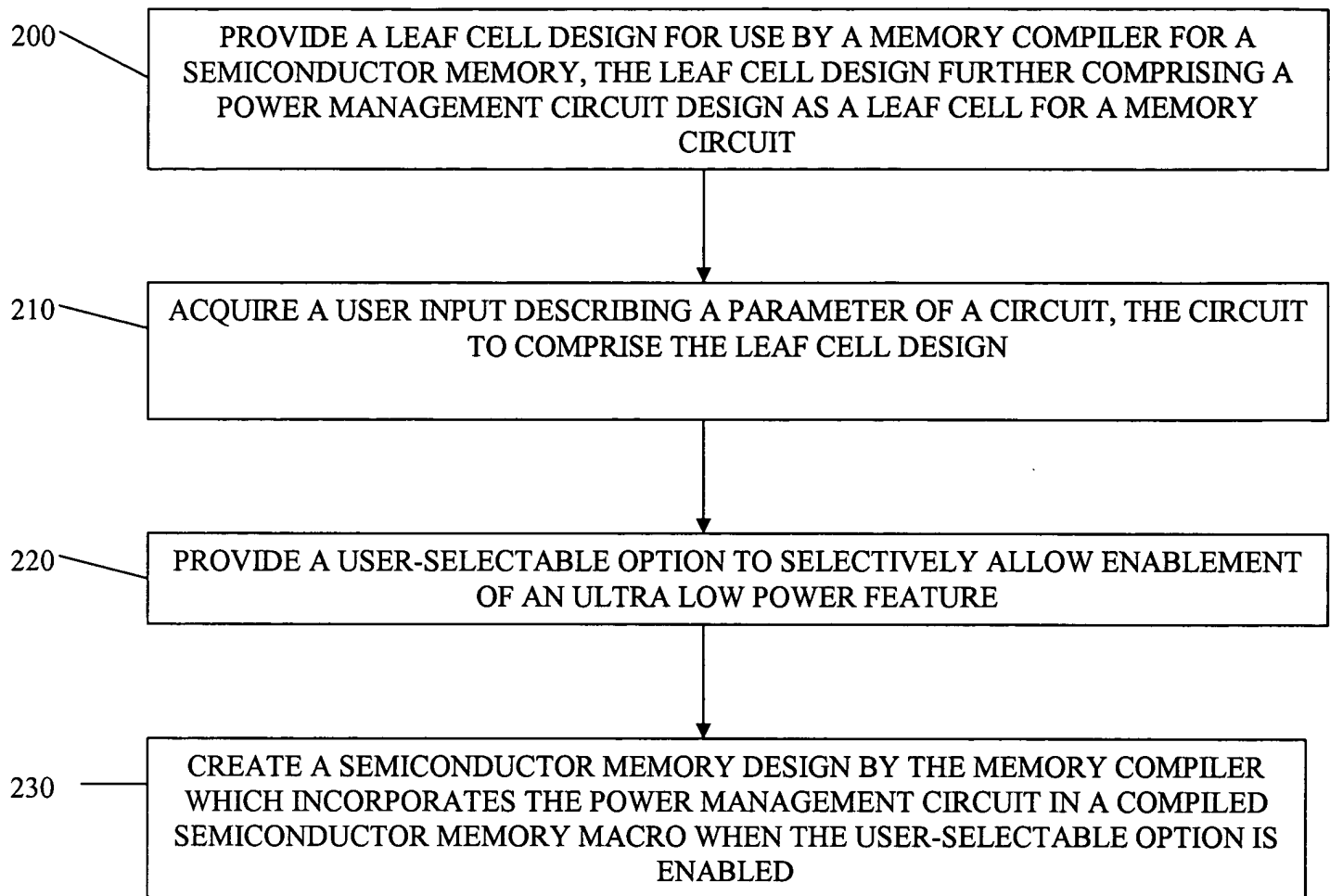


FIG. 4